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# 计算机测量与控制

COMPUTER MEASUREMENT & CONTROL

2016

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第24卷



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ITECH ELECTRONICS

中国部 TEL:4006-025-000  
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社 长: 奚全生  
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Gou Yongming

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Meng Hancheng

## English Editor:

Yang Lisheng

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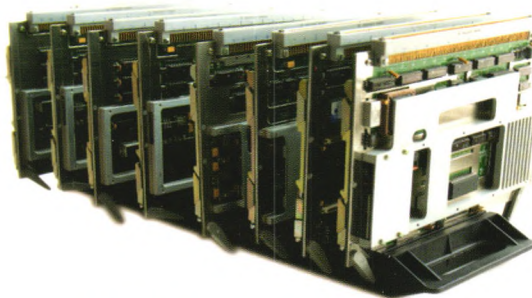
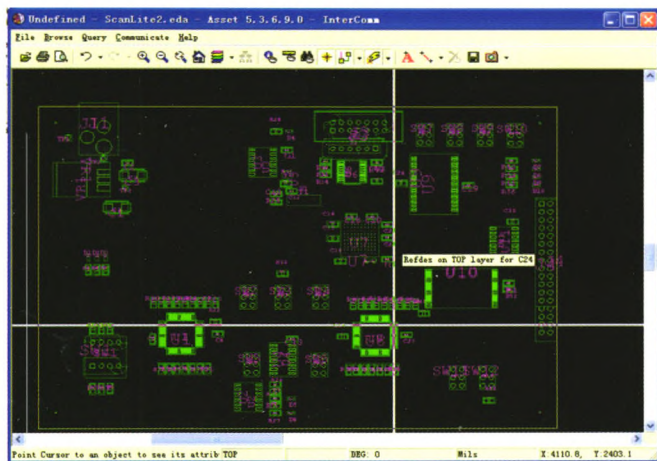
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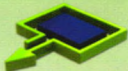
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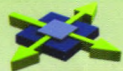
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Boundary-Scan Test

Verify interconnects and provide access to ICs on a board



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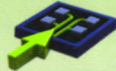
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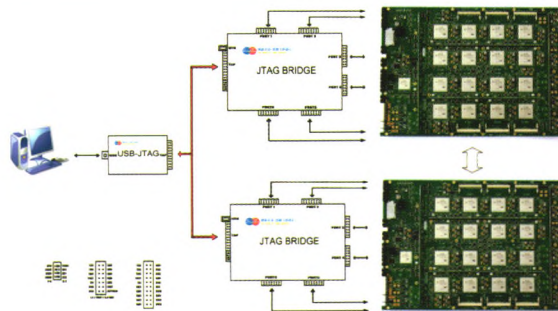
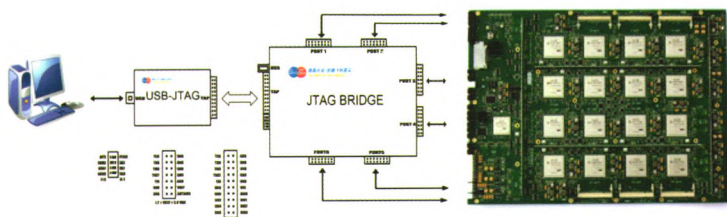
**FCT**  
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Insert temporary test IP blocks into on-board FPGAs to test board functions



**IJTAG**  
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Control embedded instruments with the standard JTAG interface on SoCs and ASICs



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地址：北京市海淀区西二旗西路领秀新硅谷1号院12号楼101#  
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