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COMPUTER MEASUREMENT & CONTROL

2016

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第24卷



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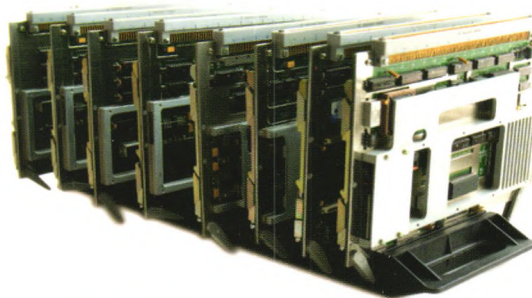
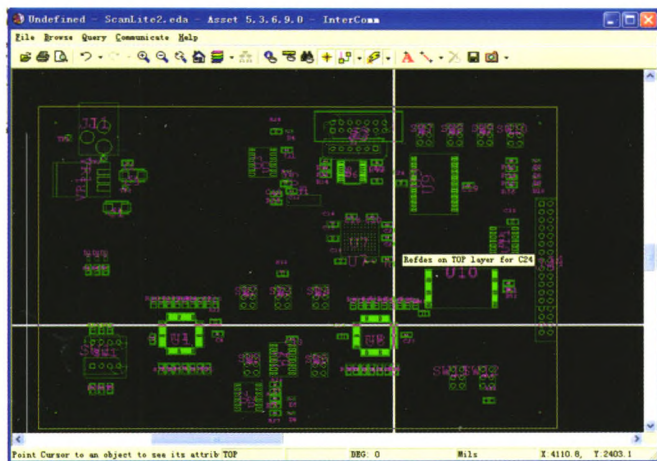
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边界扫描 测试技术

Boundary Scan Test Technology

专业电路板故障检测工具



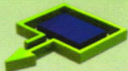
- 快速有效解决开路、短路故障。
- 定位精确，可具体到器件管脚。

ASSET
Driving Embedded Instrumentation

JTAG测试领导者

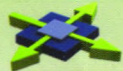
From 1995

SCANWORKS
Platform for Embedded Instruments



BST
Boundary-Scan Test

Verify interconnects and provide access to ICs on a board



PCT
Processor-Controlled Test

Control the μ Processor to enable functional at-speed test and debug a board



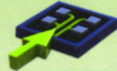
HSIO
High-Speed I/O Validation

Validate high-speed serdes links between ICs and chipsets e.g. Intel®



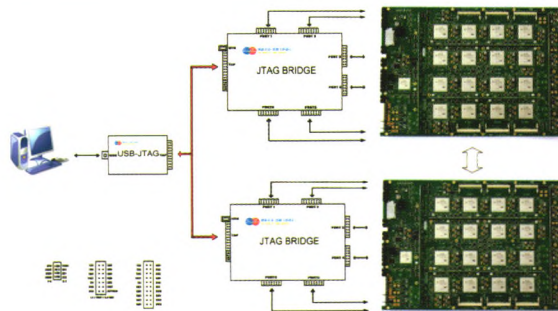
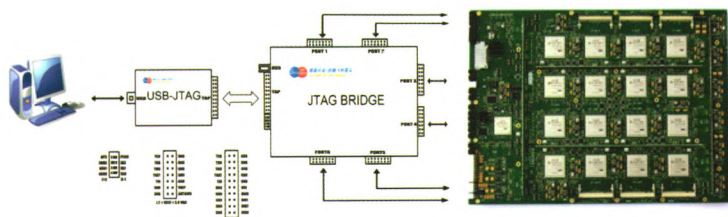
FCT
FPGA-Controlled Test

Insert temporary test IP blocks into on-board FPGAs to test board functions



IJTAG
Internal JTAG Test

Control embedded instruments with the standard JTAG interface on SoCs and ASICs



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