

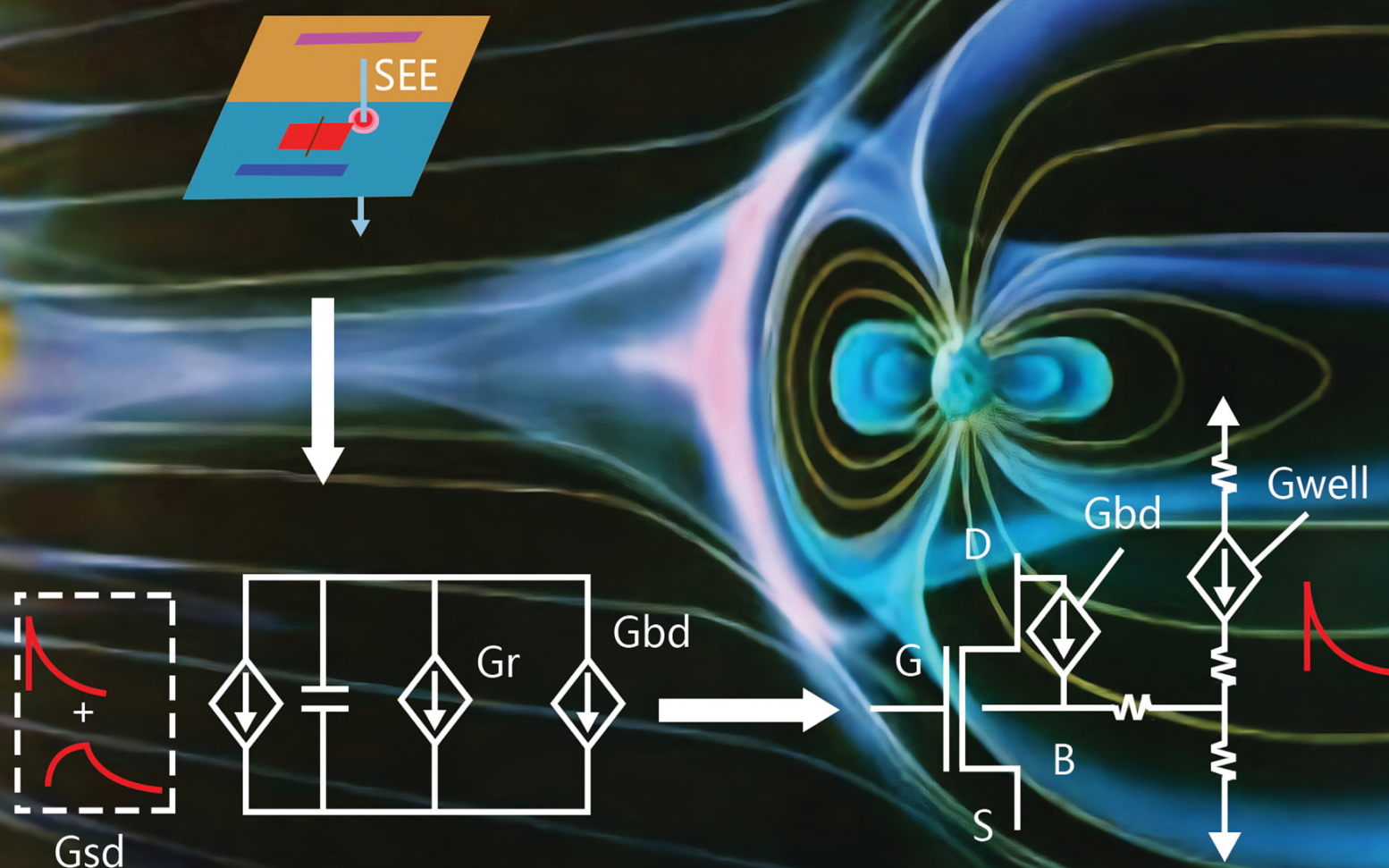
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Circuit-level Modeling and Simulation of Single Event Effects in CMOS Electronics

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